

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Peter Pegler	Confirmation No.:	9290
Patent No.:	6,812,079	Issue Date:	November 2, 2004
Serial No.:	10/677,570	Filing Date:	October 1, 2003
For:	Method for a Junction Field Effect Transistor with Reduced Gate Capacity	Attorney Docket No.:	65860-5028-US (formerly LOVO-041.DIV)

STATEMENT UNDER 37 C.F.R. § 3.73(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Sir:

Lovoltech Inc., a corporation, states that it is the assignee of the entire right, title and interest in the patent application/patent identified above by virtue of an assignment from the inventor(s) of the patent application/patent identified above.

The assignment was recorded in the United States Patent and Trademark Office on October 22, 2002 at Reel 013419, Frame 0698, or for which a copy thereof is attached.

The undersigned is authorized to act on behalf of the assignee.

Date: November 17, 2006



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